



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anatoliy V. Tsyrganovich

Assignee: ZiLOG, Inc.

Title: "ADC With Reduced Quantization Noise and Programmable Bit Resolution"

Appl. No.: 10/821,517

Filing Date: April 9, 2004

Examiner: Lam T. Mai

Art Unit: 2819

Docket No.: ZIL-537-1P

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July 6, 2005

COMMISSIONER FOR PATENTS  
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**TERMINAL DISCLAIMER OVER AN ISSUED PATENT  
PURSUANT TO 37 C.F.R. § 1.321(c)**

The owner, ZiLOG, Inc., of a one hundred percent interest in the instant application hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term, including the patent term adjustment, defined in 35 U.S.C. 154 to 156 and 173, of U.S. Patent No. 6,839,010, filed on December 27, 2002. The owner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and U.S. Patent No. 6,839,010 are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

In making the above disclaimer, the owner does not disclaim the terminal part of any patent granted on the instant application that would extend to the expiration date of the full statutory term as defined in 35 U.S.C. 154 to 156 and 173 of U.S. Patent No. 6,839,010, in the event that U.S. Patent No. 6,839,010:

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expires for failure to pay a maintenance fee, is held unenforceable, is found invalid by a court of competent jurisdiction, is statutorily disclaimed in whole or terminally disclaimed under 37 CFR 1.321, has all claims canceled by a reexamination certificate, is reissued, or is in any manner terminated prior to the expiration of its full statutory term as presently shortened by any terminal disclaimer.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

The undersigned is an attorney or agent of record. The terminal disclaimer fee under 37 CFR 1.20(d) is included.

Date: July 6, 2005

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